

AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A commutator ~~Commutator~~ arrangement for a small electric motor, comprising:

~~with a~~ capacitor ring disk consisting of several capacitor elements, ~~wherein the said~~ capacitor ring disk comprising ~~is made of~~ a dielectric first ring disk (RS); on whose upper side several sector-shaped electrode layers (6) are positioned, ~~wherein each of the electrode layers (6) is being~~ electrically conductively connected with a commutator segment (13), and wherein counter electrodes of the capacitor ~~element elements~~ are provided on an underside of the ring disk (RS), which is located opposite the upper side of the ring disk (RS),

~~characterized in that~~

wherein a single underlying counter electrode layer (4) is provided, said counter electrode layer (4) which underlies and almost covers the electrode layers (6) so that the counter electrodes of all capacitor elements have the same potential.

2. (Currently Amended) ~~Commutator~~ The commutator arrangement as defined in claim 1, wherein a plurality of capacitor ring disks are laminated on top of one another with a dielectric layer (15) disposed between adjacent capacitor ring disks ~~in between like a multilayer capacitor.~~

3. (Currently Amended) The commutator ~~Commutator~~ arrangement as

defined in ~~one of the preceding claims~~claim 2, wherein the capacitor ring disks are laminated one on top of the other so that the electrode layers (6) are essentially located congruently over each other.

4. (Currently Amended) The commutator ~~Commutator~~ arrangement as defined in ~~one of the preceding claims~~claim 2, wherein the capacitor elements created by the electrode layers (6) stacked on top of each other are contacted together to create a capacitive element and are each connected with a commutator segment (13).

5. (Currently Amended) The commutator ~~Commutator~~ arrangement as defined in ~~one of the preceding claims~~claim 2, wherein the electrode layers (6) placed on top of one another are contacted with each other on an exterior ~~circumference~~ circumferential surface of the first ring disk (RS).

6. (Currently Amended) The commutator ~~Commutator~~ arrangement as defined in ~~one of the preceding claims~~claim 1, wherein the counter electrode layer (4) is round and extends up to an interior circumference of the first ring disk (RS).

7. (Currently Amended) The commutator ~~Commutator~~ arrangement as defined in ~~one of the preceding claims~~claim 1, wherein the counter electrode layer (4) is formed as resistance layer with a resistance in the range from 0.1 Ω to 1 K Ω .

8. (Currently Amended) The commutator ~~Commutator~~ arrangement as defined in ~~one of the preceding claims~~claim 1, wherein at least one inductor is connected before or after each capacitor element or each capacitive element in series.

9. (Currently Amended) The commutator ~~Commutator~~ arrangement as defined in ~~one of the preceding claims~~claim 1, wherein at least one discrete inductor is connected before or after each capacitor element or each capacitive element in series.

10. (Currently Amended) The commutator ~~Commutator~~-arrangement as defined in ~~one of the preceding claims~~claim 1, ~~further comprising wherein a~~ second ring disk made of a highly permeable magnet material, said second ring disk being ~~is~~-provided for which each of the inductors is formed by a conductive structure placed thereon, and wherein each of the capacitances is electrically conductively connected with one of the inductors.

11. (Currently Amended) The commutator ~~Commutator~~-arrangement as defined in ~~one of the preceding claims~~claim 1, wherein at least one resistance is connected before or after each capacitor element or each capacitance in series.

12. (Currently Amended) The commutator ~~Commutator~~-arrangement as defined in ~~one of the preceding claims~~claim 11, wherein the resistance has a non linear U/I characteristic curve.

13. (Currently Amended) The commutator ~~Commutator~~-arrangement as defined in ~~one of the preceding claims~~claim 11, wherein the resistances are designed in one piece in the form of a third ring disk or as a further resistance layer placed on a ring disk or on the electrode layer (6) or the counter electrode layer (4).

14. (Currently Amended) The commutator ~~Commutator~~-arrangement as defined in ~~one of the preceding claims~~claim 11, wherein the second and/or the third ring disk (16) or resistance layer form a laminate (L) with the capacitor ring disk(s).

15. (Currently Amended) The commutator ~~Commutator~~-arrangement as defined in ~~one of the preceding claims~~claim 14, wherein a supporting disk (11) made of ceramic is provided for the retention of the capacitor or the laminate (L).

16. (Currently Amended) The commutator ~~Commutator~~-arrangement as defined in ~~one of the preceding claims~~claim 14, wherein the capacitor ring disk(s) or the laminate (L) are connected with a commutator sleeve (12) supporting the

commutator segments (13).

17. (Currently Amended) The commutator ~~Commutator~~ arrangement as defined in ~~one of the preceding claims~~ claim 14, wherein the capacitor ring disk(s) or the laminate (L) is surrounded by the commutator segments (13).

18. (Currently Amended) A small ~~Small~~ electric motor with a shaft and a commutator arrangement mounted thereon as defined in claim 1 ~~one of the preceding claims~~.